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Date: 05/03/2022

## Report on one day workshop on Awareness on Virtual Lab & Its Utilization for 3<sup>rd</sup> Semester Computer Science & Engineering Students

A one-day workshop on "Awareness on Virtual Lab and its utilization" was organized for the 3<sup>rd</sup> Semester Students of Computer Science & Engineering Students on 05/03/2022.

The main Aim of the workshop is to bring the awareness on the Virtual Lab and utilization in the regular curriculum. The workshop was inaugurated by **Dr. Govinde Gowda M S,** Dean Academics, ATME College of Engineering, Mysuru. **Dr. Puttegowda D,** Prof. & Head Department of Computer Science & Engineering, ATME College of Engineering, Mysuru presided over the function.



Dr. Govinde Gowda M S addressing the students during the workshop

In his inaugural address, sir spoke about the importance of learning and how virtual lab is contributing has a blended learning tool by enhancing the better understanding of the concepts learnt. Sir also insisted the students to go through the experiments of the V-labs before actually conducting the experiment in the physical lab. This will bring a better understanding and the deep learning among the concepts learnt. Sir also informed the students to use the virtual labs not only for the lab component but also this can be used to substitute the theory concepts learnt in the few of the courses in the regular curriculum.



Dr. Puttegowda D addressing the students during the workshop

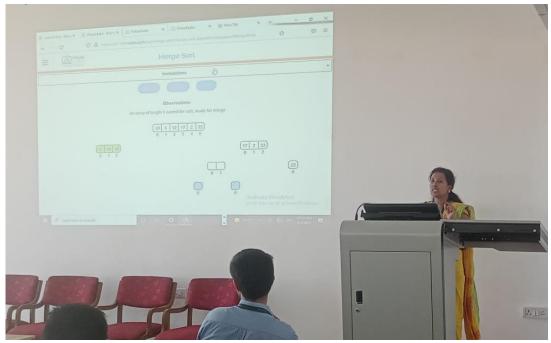
Later the session was addressed by **Dr. Puttegowda D, Dept. of Computer Science & Engineering, ATMECE, Mysuru.** In his address he motivated the students to make utilization of the platform provided through Virtual Labs and explore the technology. He also appreciated the interest shown by the student of Dept. of CSE for their active participation in the event.



Prof. Thejkumar J addressing the students during the workshop

**Prof. Thejkumar J, Nodal Centre Coordinator, V- Labs, ATMECE, Mysuru** addressed the student by briefing about the idea behind the coming out of the concept of Virtual labs and the initiatives taken by the ATME college of Engineering in implementation of V-labs in the regular curriculum.

Also, the students were taken to hands on session on the Virtual lab portal, where they were guided about how to use the web portal, search for experiments, choosing the experiment to conduct and its simulation.



Prof. Sushma V, demonstrating the experiments during the workshop

**Prof. Sushma V, Department Coordinator, V – Labs, Dept. of CSE, ATMECE, Mysuru** taken over the second session. She demonstrated the program and simulations on merge sort under the data structures lab. She also elaborated on the programming logic and flow that was designed in Virtual Labs and motivated the students to utilize Virtual Lab platform in their regular curriculum subjects.

Students worked on the Virtual lab platform and had hands on experience in conducting experiments and learnt the navigation in Virtual lab Platform.

Sl. No.	Lab Conducted	Coordinator	Department
1	Data Structures - Merge Sorting	Sushma V	Computer Science & Engineering

## **Report Prepared by**

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